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<https://scholar.google.bg/citations?hl=bg&user=t5cg68YAAAAJ>

<https://www.scopus.com/authid/detail.uri?authorId=57206666375>

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<https://www.researchgate.net/profile/Valeri-Mladenov>

<https://www.webofscience.com/wos/author/record/240520>

**Valeri Mladenov (M'14, SM'21)** graduated in Electrical Engineering (with distinction) from the Higher Institute for Mechanical and Electrical Engineering, Sofia (at present Technical University of Sofia), Bulgaria in 1985. He received his Ph.D. from the same institution in 1993. In 2019 he defends a "Doctor of Sciences" thesis. Since 1986 he has been a lecturer in the Department of Fundamentals of Electrical Engineering at the Technical University of Sofia (TUS), Bulgaria. In 1999 he became an Associate Professor. In 2004 he becomes a Head of the Department of Fundamentals of Electrical Engineering. In June, 2011 he was a Dean of Faculty of Automation. Since Dec., 2011 till Nov. 2015, he was a Vice-Rector of the Technical University Sofia. Currently he is a Head of department Fundamentals of Electrical Engineering at the Technical University of Sofia. He is a guest lecturer at the Faculty of Electrical Engineering, Eindhoven University of Technology, in the Netherlands, where he teaches a course "Nonlinear Systems and Neural Networks". He has been invited lecturer in many universities. In 2014 he has been a Deputy Minister of Education and Science in Bulgaria.

Prof. Mladenov's research interests are in the field of neural networks, artificial intelligence, nonlinear circuits and systems, electronics and signal processing. He has received many international research fellowships. He has more than 300 scientific papers in professional journals and conferences. He is a co-author of ten books and manuals for students. He has received many research grants from the Technical University of Sofia, Bulgarian Ministry of Education and Science, COST, DAAD - Germany, NWO - Netherlands, Royal Society - UK, NATO, TEMPUS and others and also with his team he participated in many national and international projects – H2020, FP7, DFG, and others. Currently he is leader of TUS team of four H2020 projects and one Erasmus+ project. He has 8 successfully defended PhD students and currently he is a supervisor of 3 PhD students. He is a founder and a head of Neurocomputing laboratory at the Technical University of Sofia.

He is a Senior Member of the Institute of Electrical and Electronics Engineering, Inc. (IEEE) <http://www.ieee.org/>, member of the IEEE Circuit and Systems Technical Committee, member of the IEEE Technical Committee on Cellular Nanoscale Networks and Memristor Array Computing (CNN-MAC) and Educational Activities Officer of the Bulgarian IEEE section. He is a Senior Member of the International

Neural Network Society (INNS) and a member of the International Council of Large Electric Systems, (SIGRE). He is also a member of the Steering Committee of the International Symposium on Theoretical Electrical Engineering (ISTET), member of Bulgarian Union of Electronic and Electrical Engineering, member of the Federation of Scientific Technical Unions in Bulgaria, a member of the Management Board of the Scientific and Technical Union of the Power Engineers, a member of the Management Board of the Union of Automation and Informatics. He is an organizer and a chair of many International Conferences and Symposiums.

As a member of several editorial boards, Prof. Mladenov serves as an editor in chief, associate editor, and reviewer for a number of professional journals and conferences. He is an Editor-in-chief of Artificial Intelligence Circuits and Systems (AICAS) Section of MDPI journal of Electronics (ISSN 2079-9292) and Editor-in-chief of the International Journal of Circuits, Systems and Signal Processing (ISSN 1998-4464). He is an Associate Editor of Springer, Evolving Systems, An Interdisciplinary Journal for Advanced Science and Technology, an Editorial Board Member of MDPI journals of Electronics (ISSN 2079-9292; CODEN: ELECGJ), Technologies (ISSN: 2227-7080), Science (ISSN: 2413-4155) and many others.

He was organizer and a general chair of two main INNS conferences namely 23rd International Conference on Artificial Neural Networks (ICANN 2013) Sofia, Bulgaria, September 10-13, 2013 and 15th International Conference on Engineering Applications of Neural Networks (EANN 2014), Sofia, Bulgaria, September 5-7, 2014.